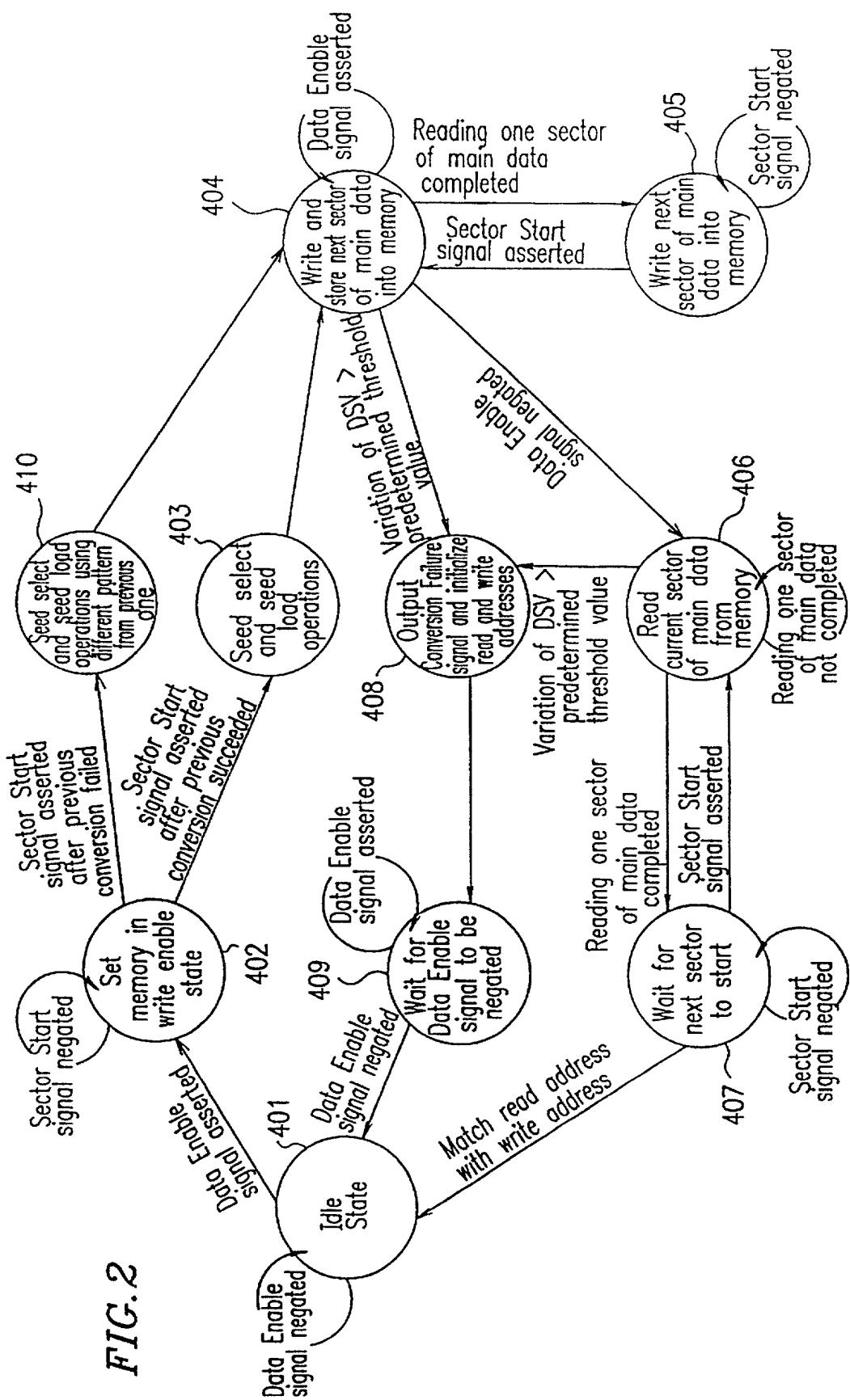
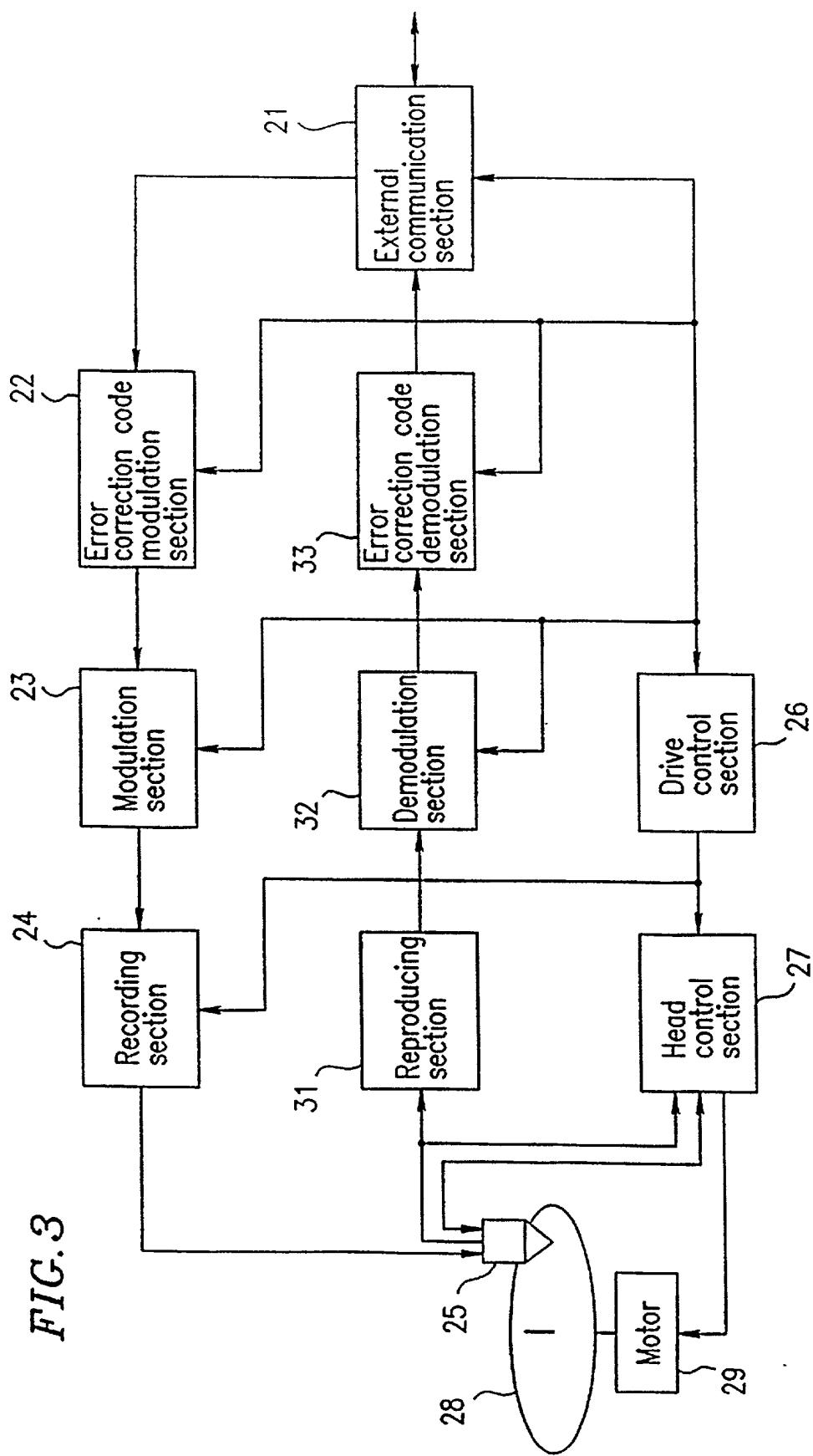


FIG. 2





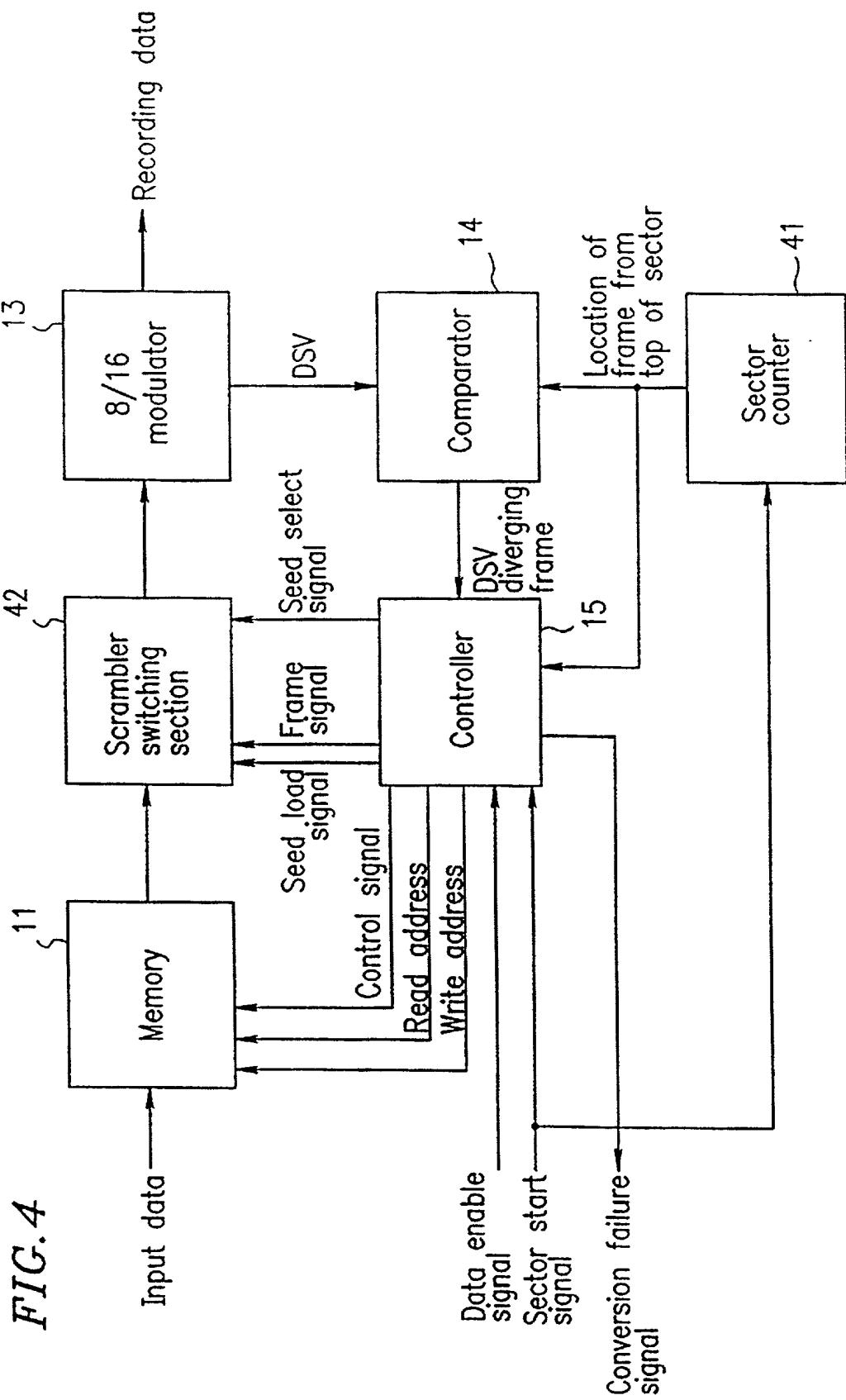


FIG. 5

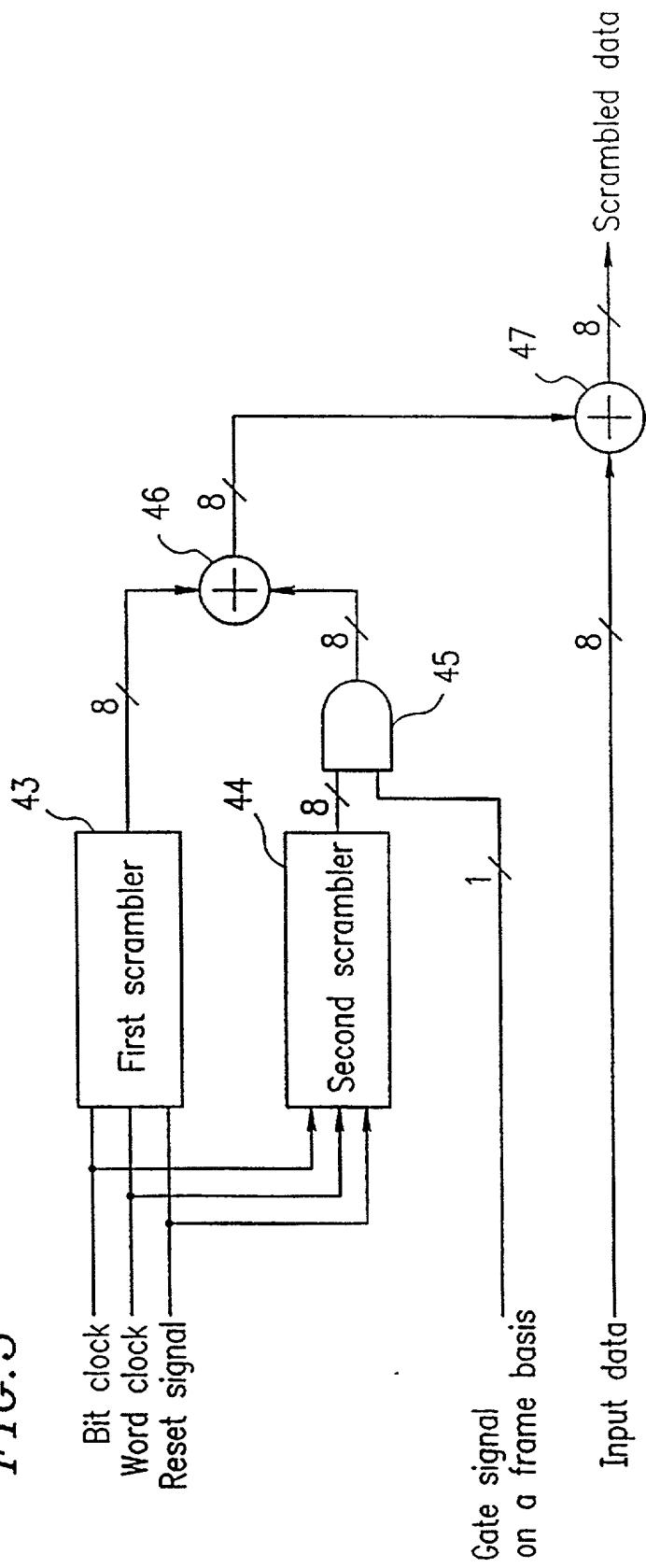
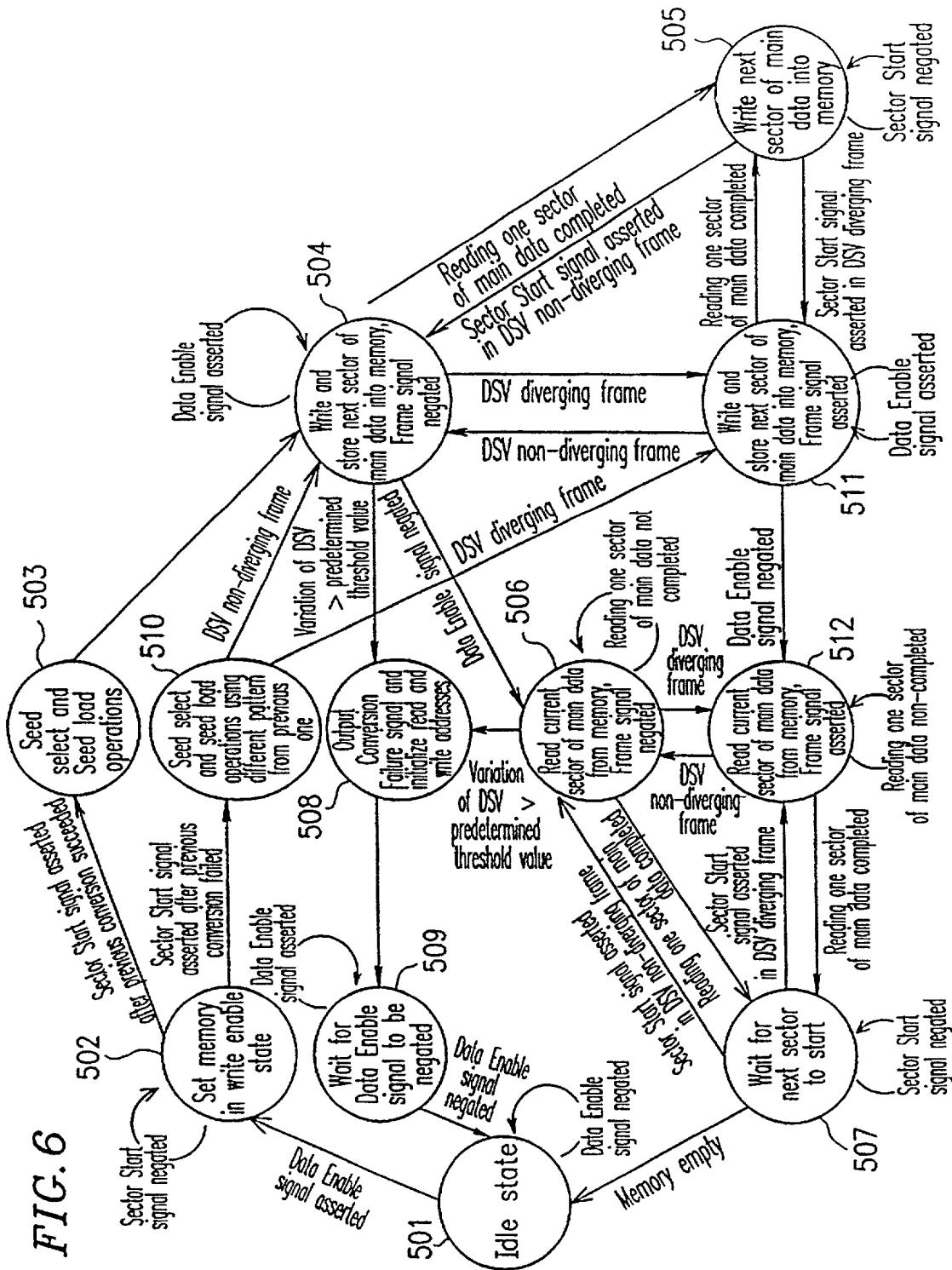


FIG. 6



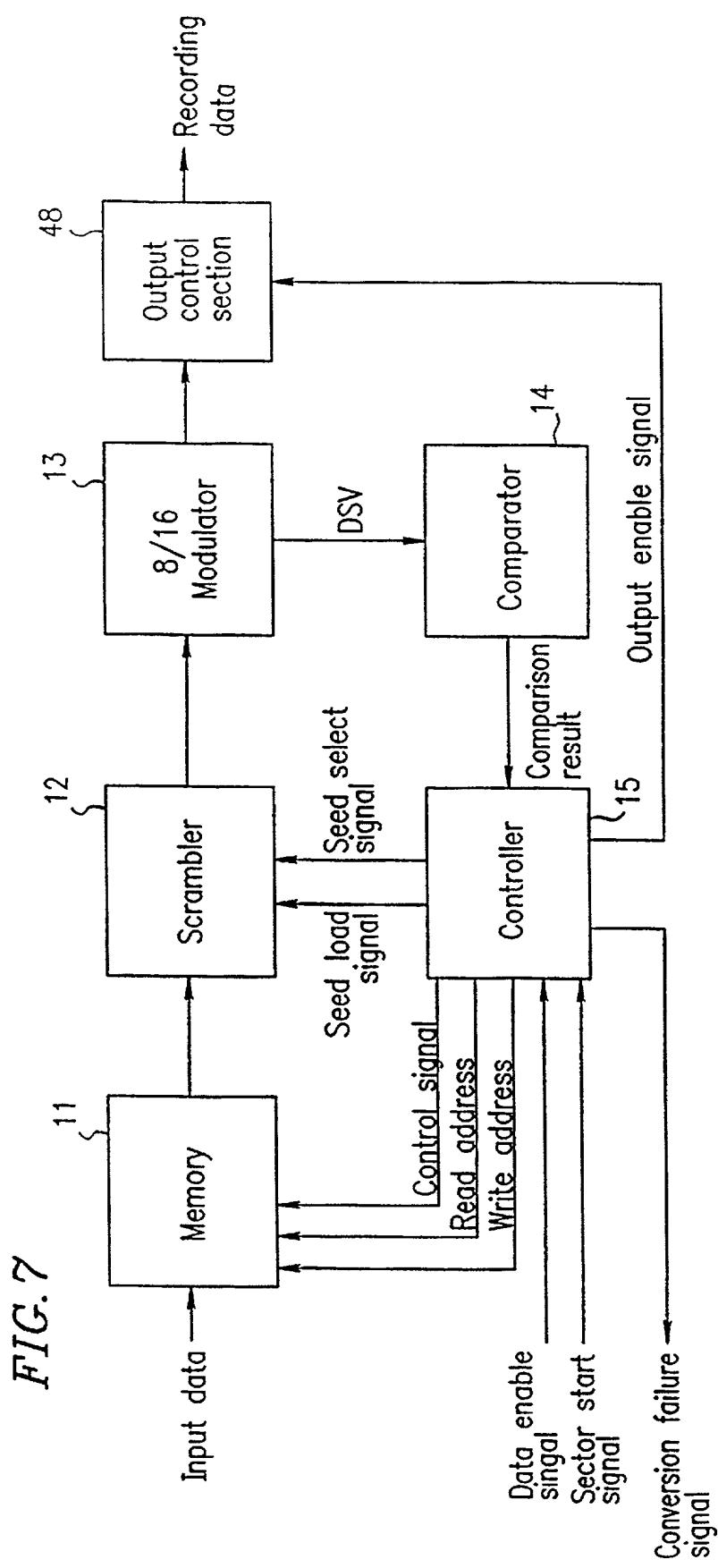


FIG. 8

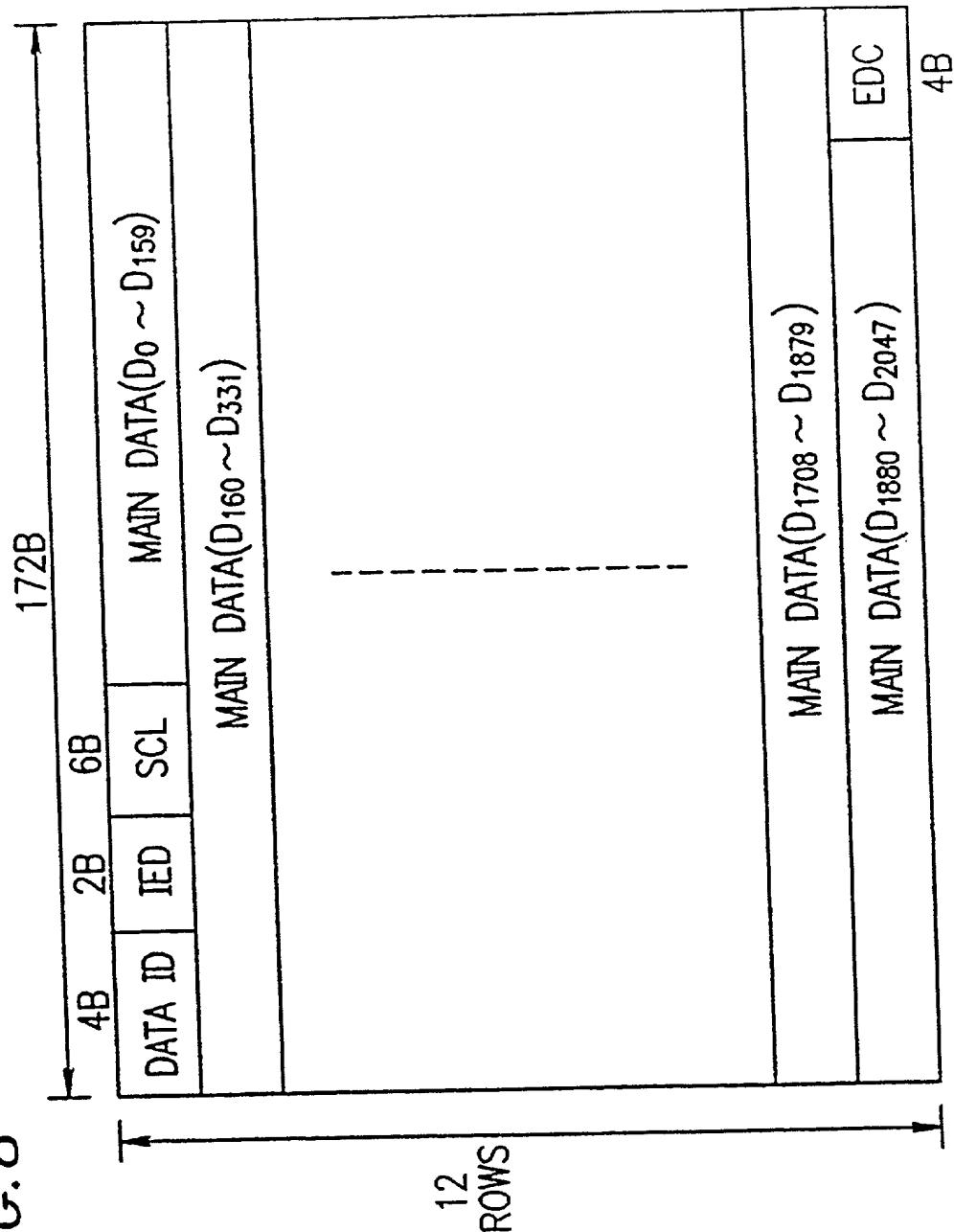


FIG. 9

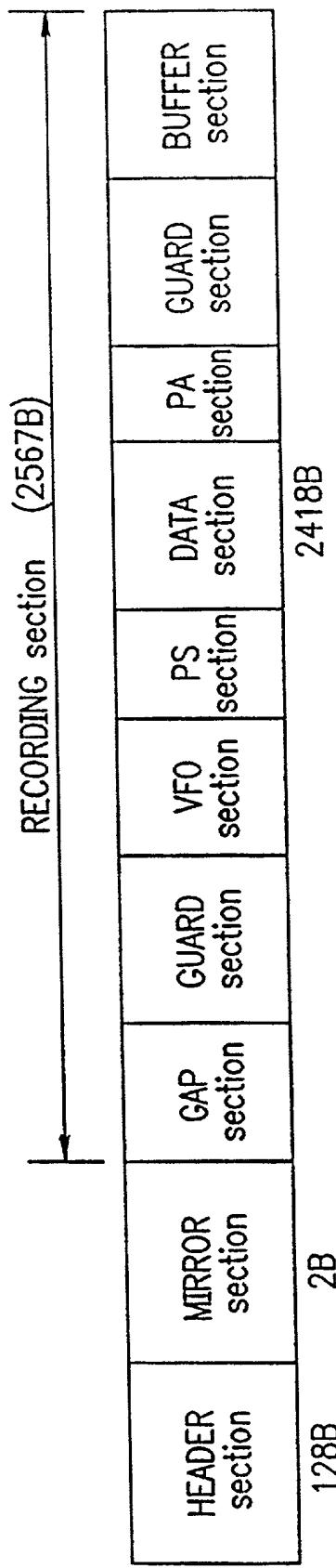


FIG. 10

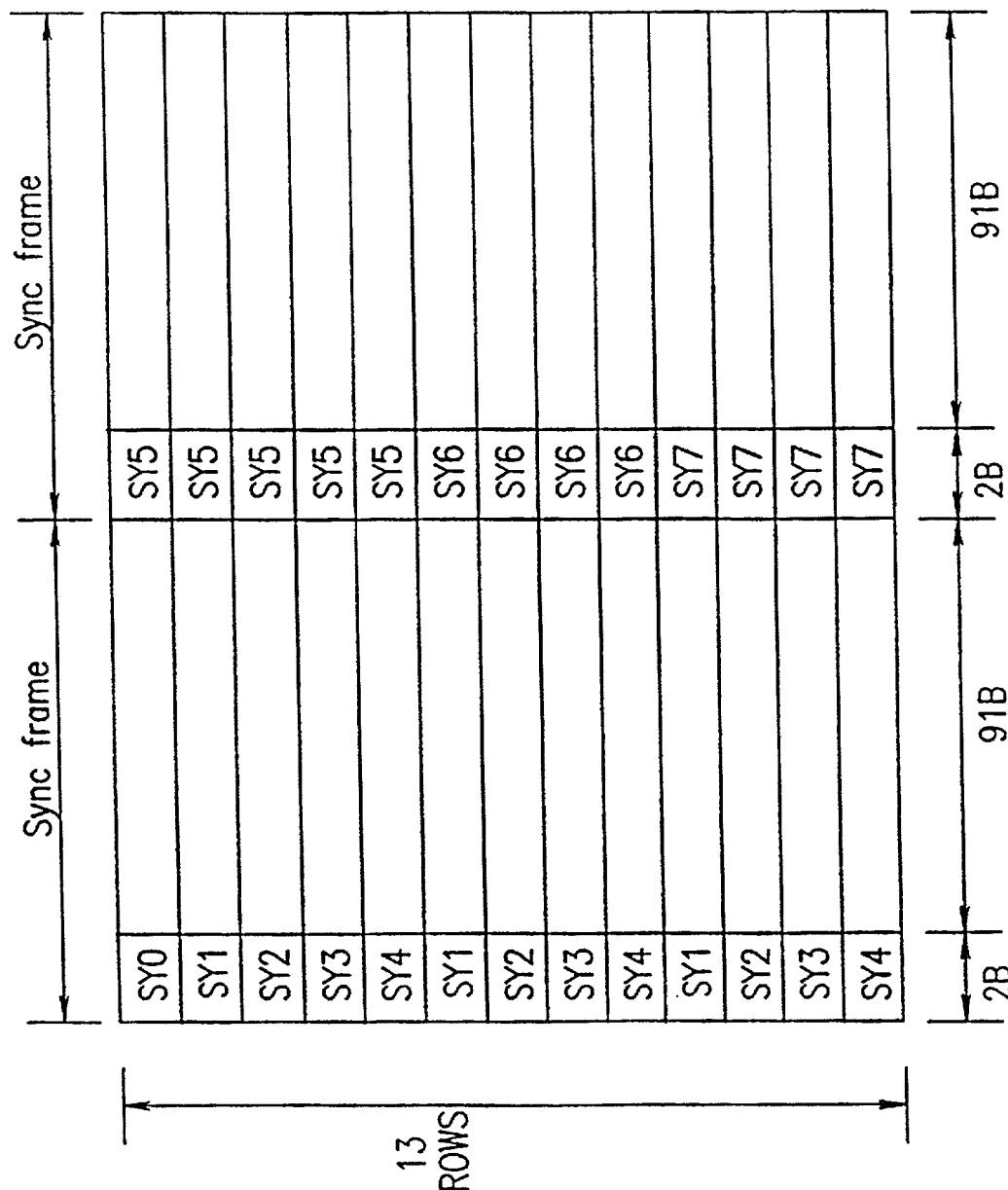
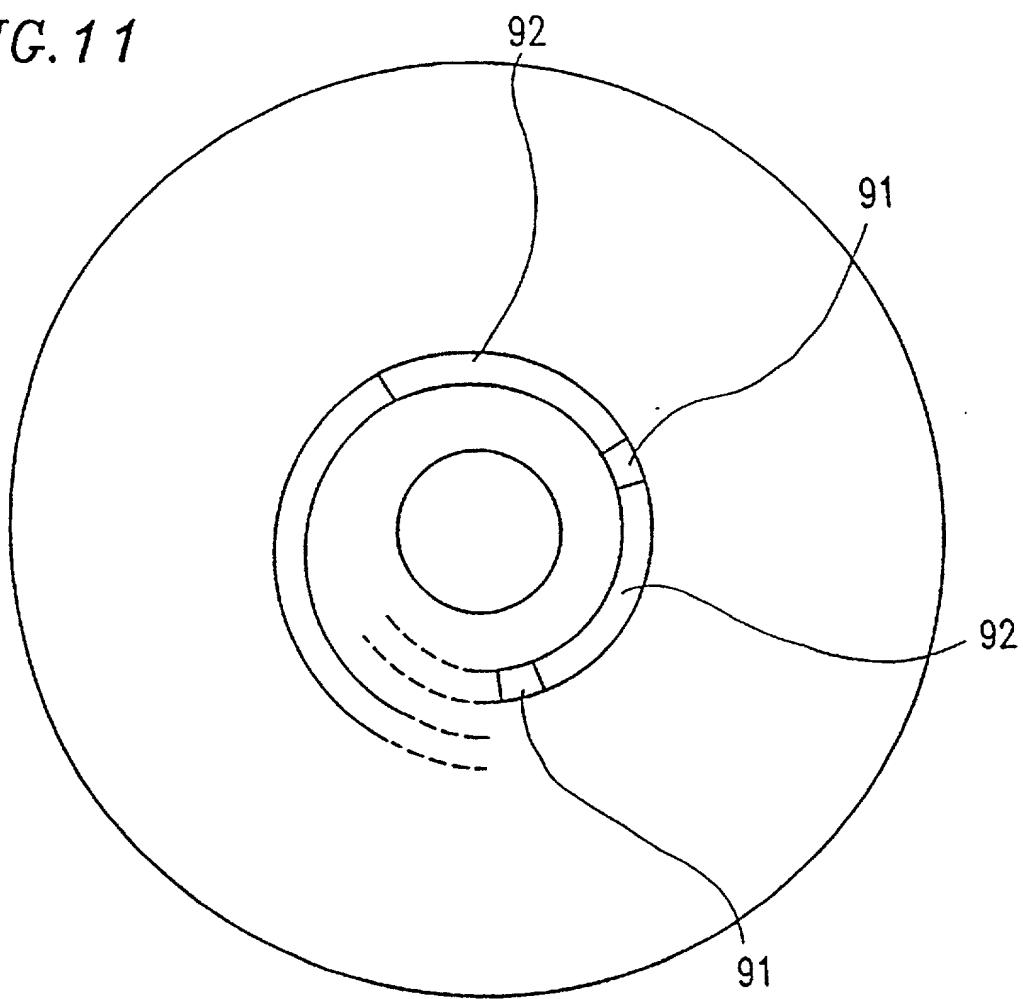
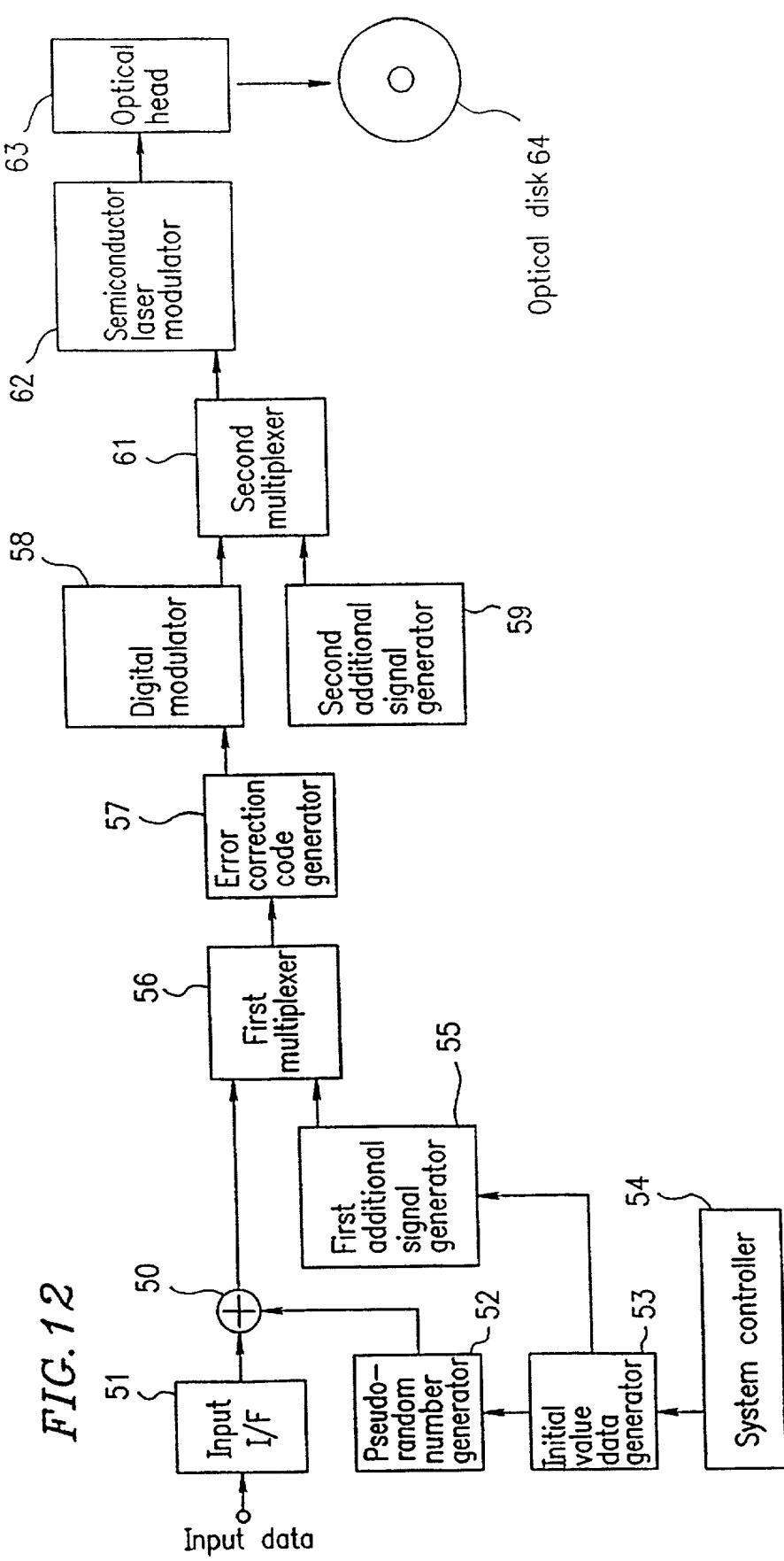


FIG. 11





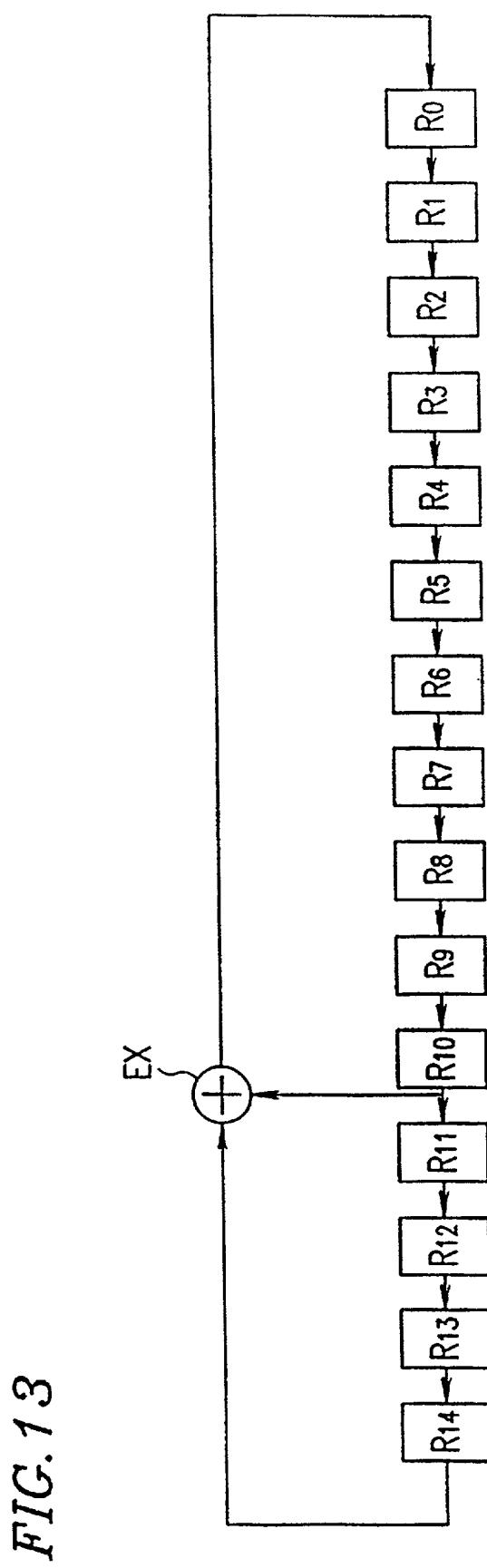


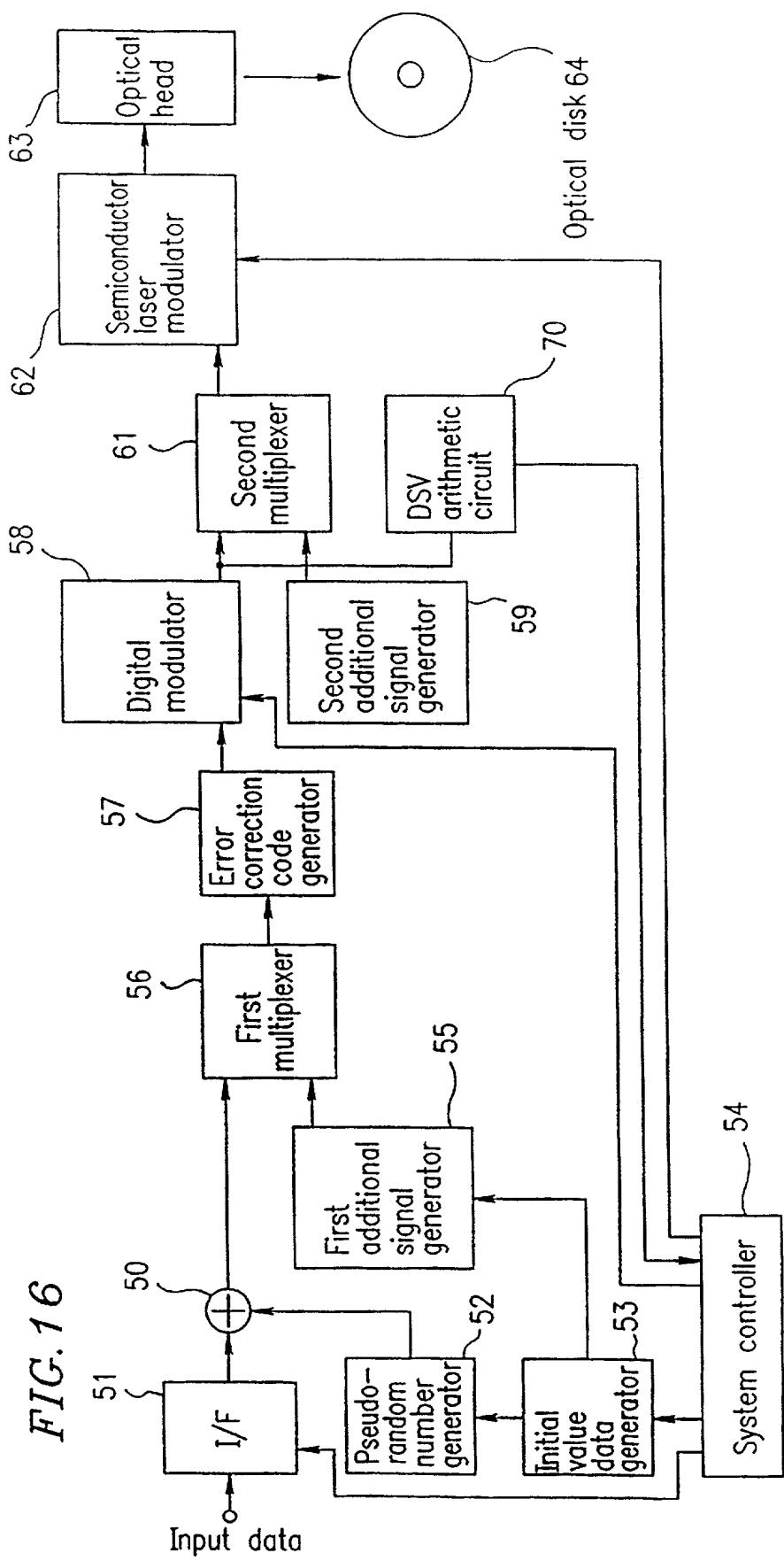
FIG. 13

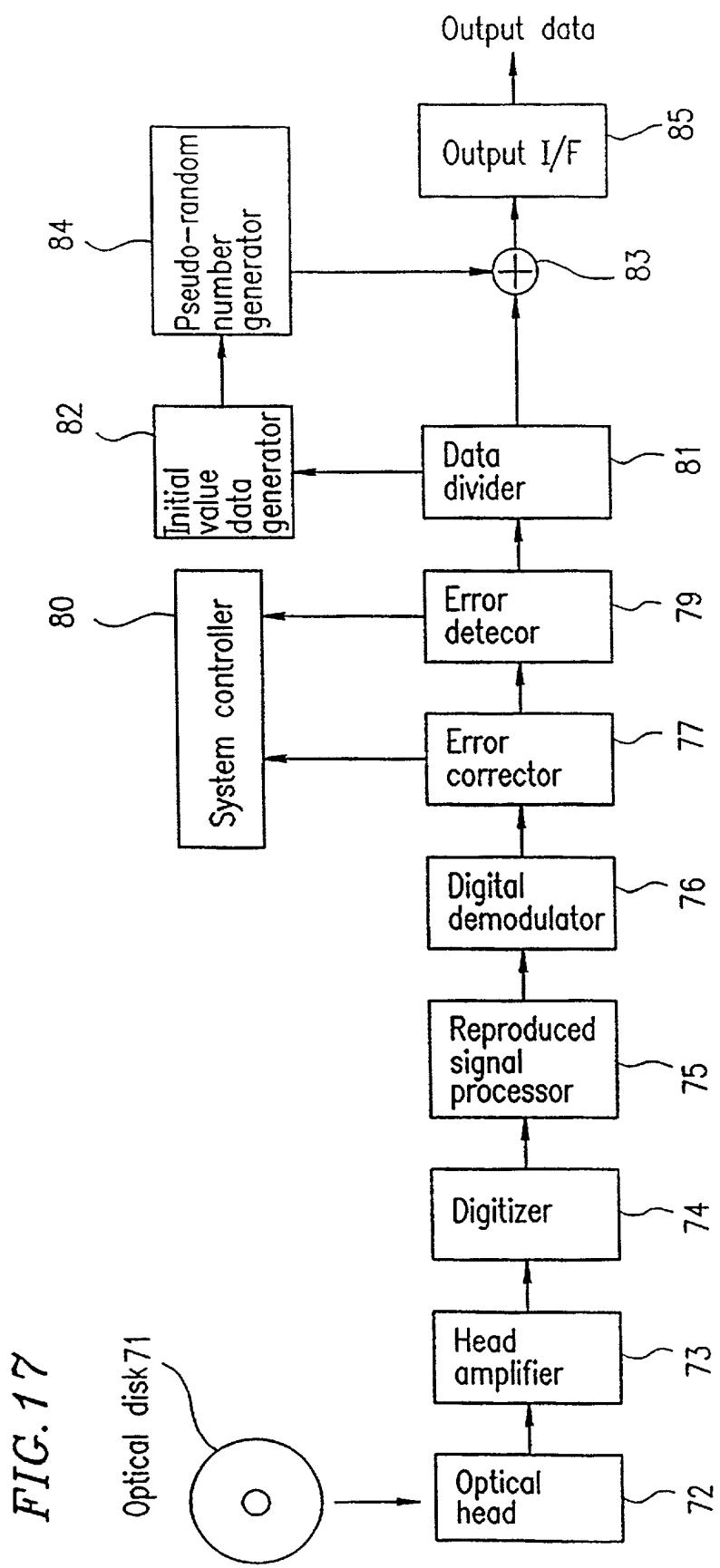
FIG. 14

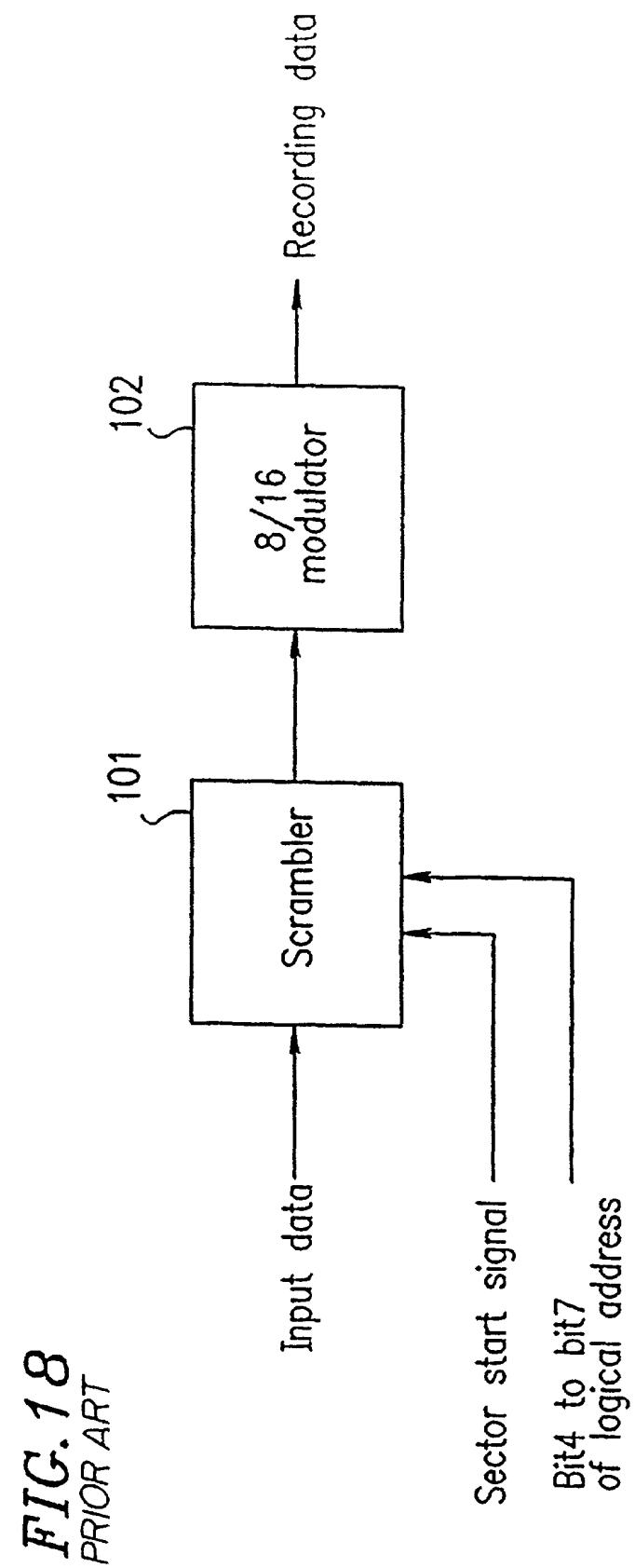
Bit string	Initial value
0h	0001h
1	5500
2	0002
3	2A00
4	0004
5	5400
6	0008
7	2800
8	0010
9	5000
A	0020
B	2001
C	0040
D	4002
E	0080
F	0005

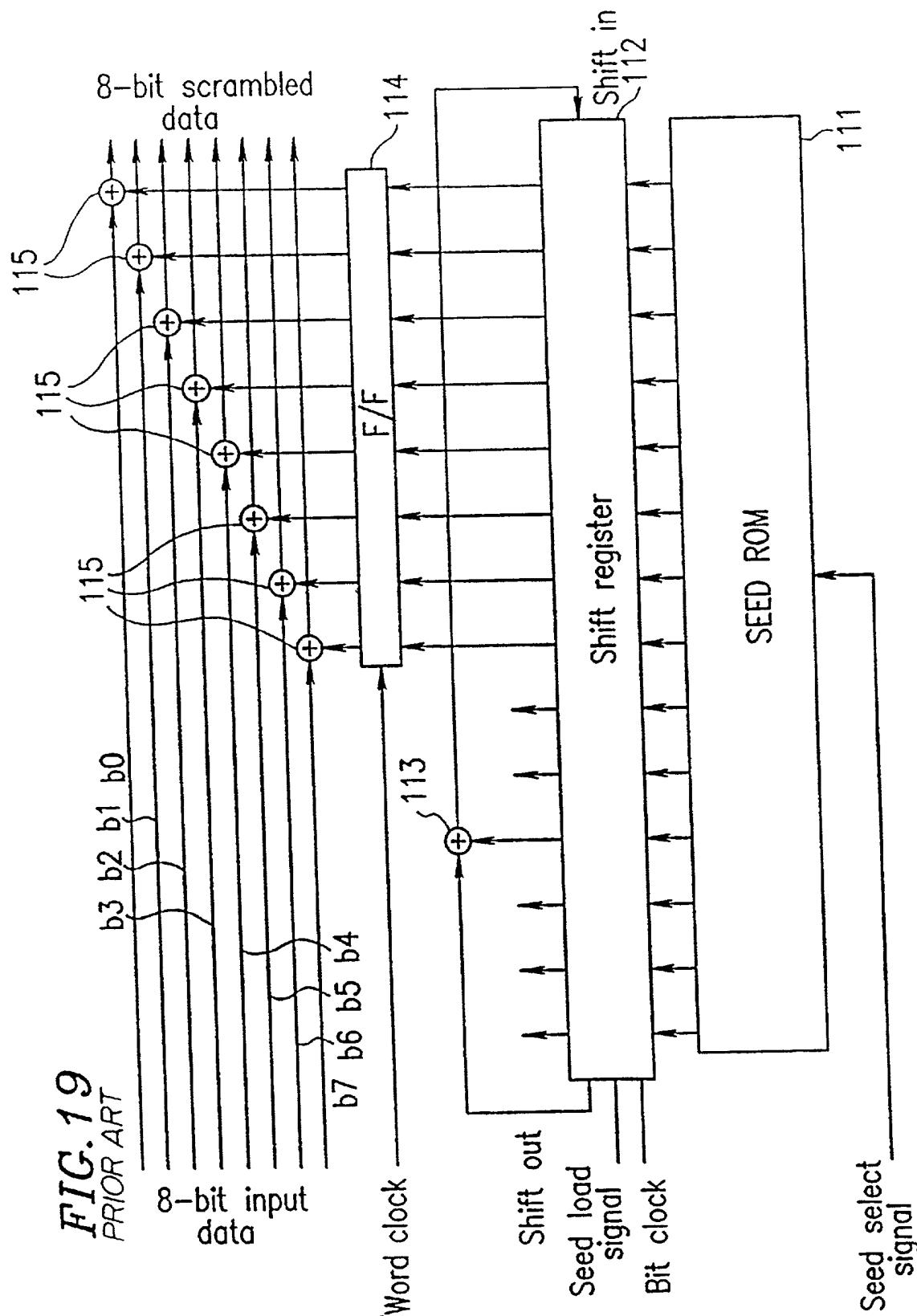
FIG. 15

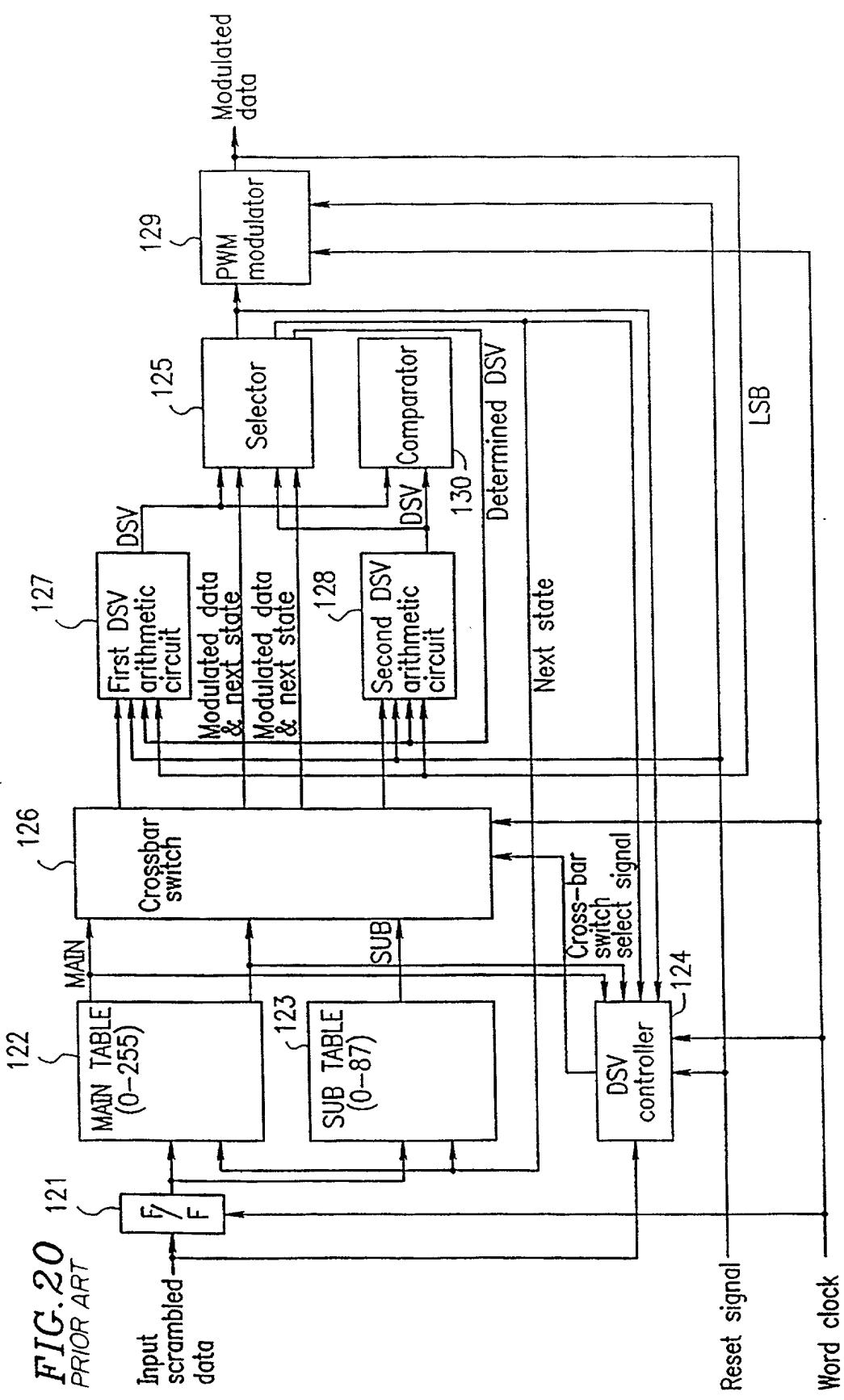
Bit string	Initial value	Rewrite repetition times 0	Rewrite repetition times 1	Rewrite repetition times 2	Rewrite repetition times 15
0h	0001h	0090h	0003h	---	0031h
1	5500	3C00	2400	---	2000
2	0002	00A0	0005	---	0032
3	2A00	1E00	1200	---	1600
4	0004	00C0	0006	---	0034
5	5400	3B00	2300	---	2B00
6	0008	00F0	0007	---	0038
7	2800	1D00	1100	---	1300
8	0010	0009	0030	---	0013
9	5000	3A00	2500	---	2A00
A	0020	000A	0050	---	0023
B	2001	1003	3001	---	2007
C	0040	0000	0060	---	0043
D	4002	2006	6002	---	400E
E	0080	000F	0070	---	0083
F	0005	00A3	0052	---	00A2











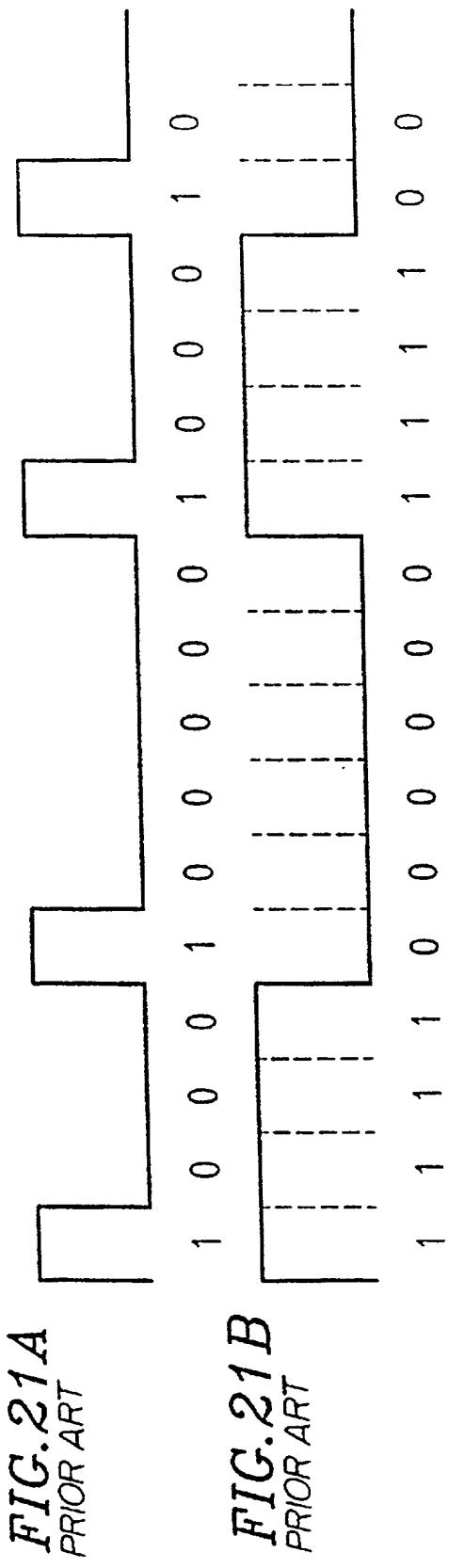


FIG. 22
PRIOR ART

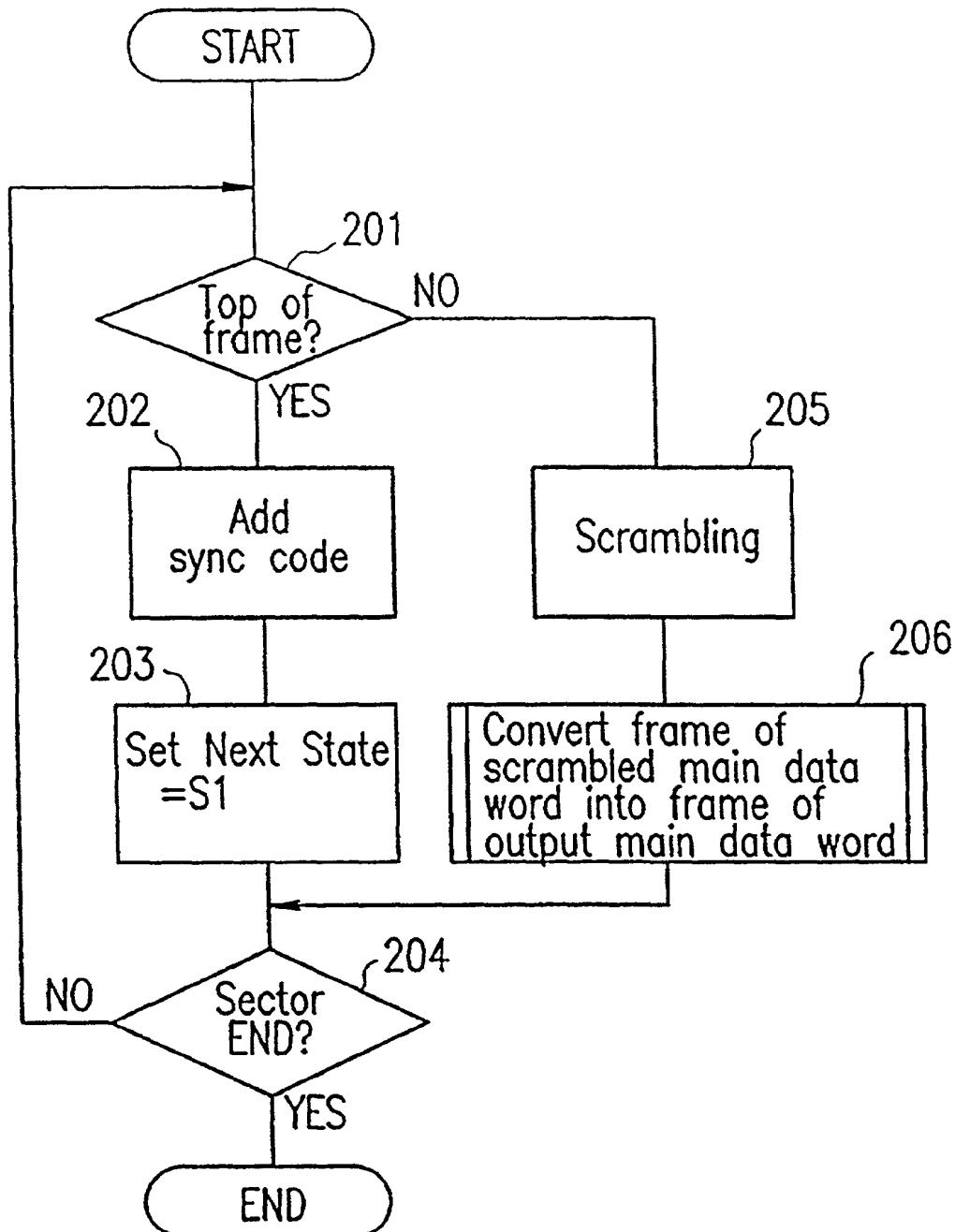


FIG. 23
PRIOR ART

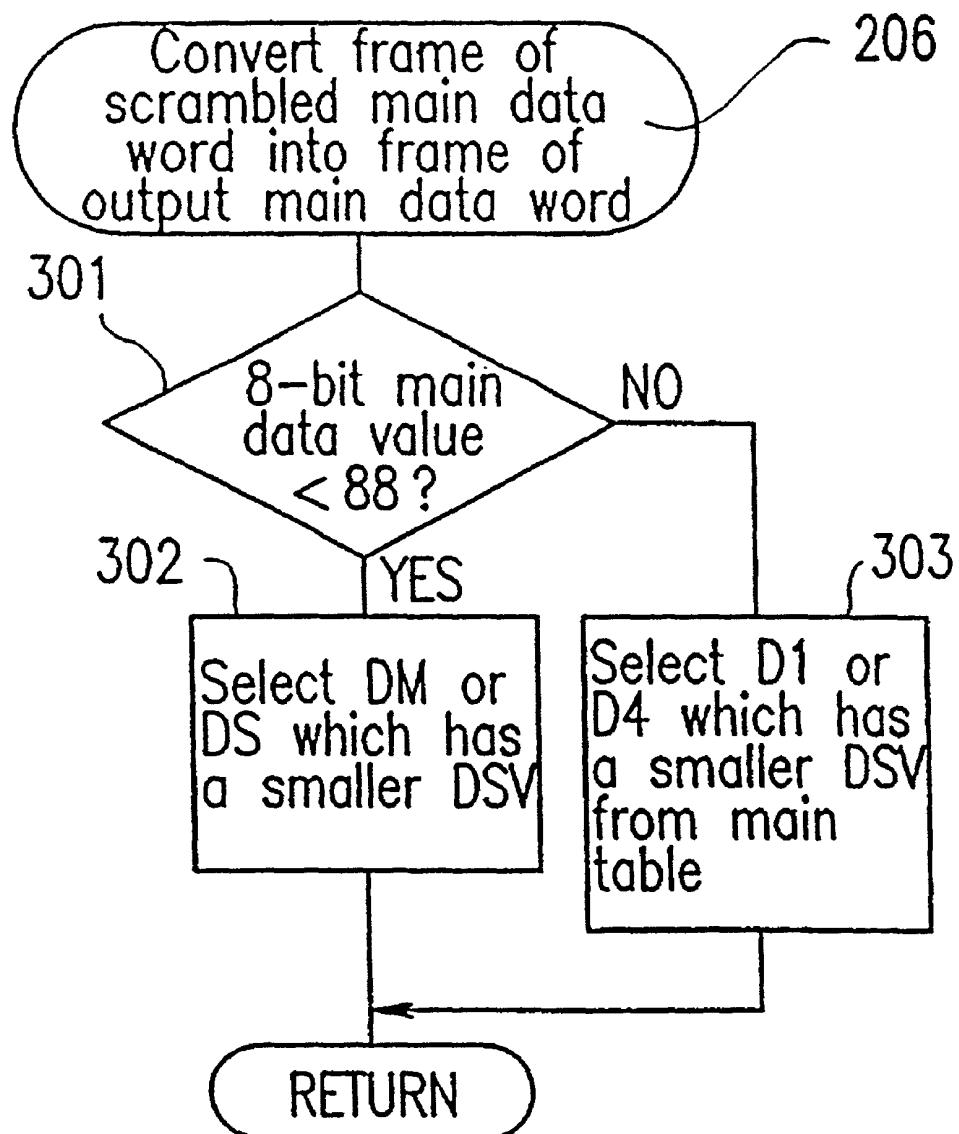


FIG. 24A

PRIOR ART

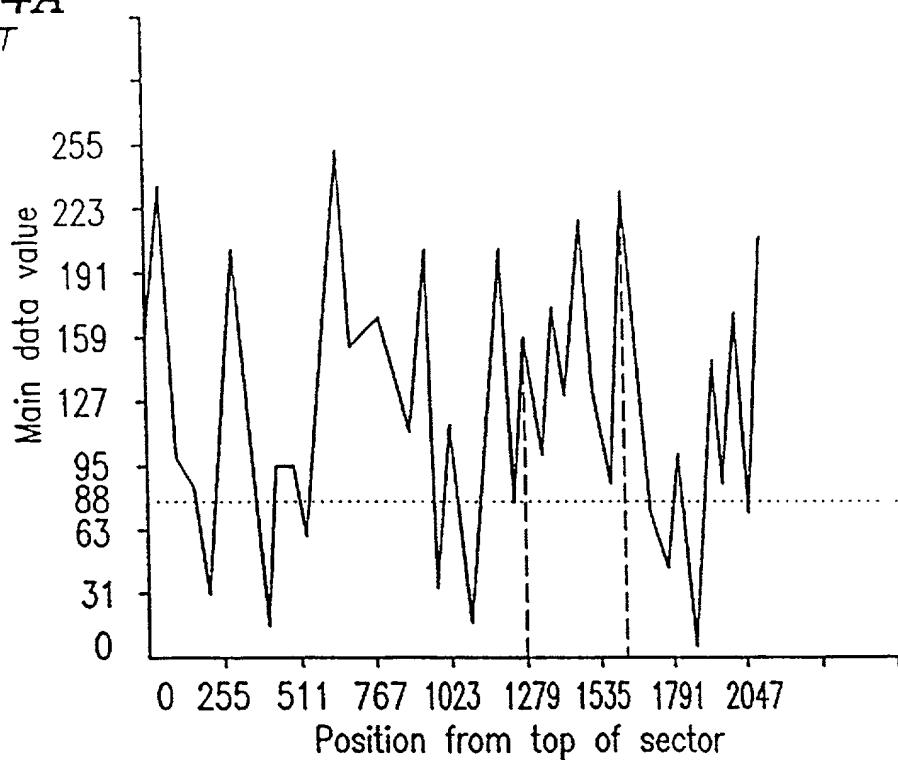


FIG. 24B

PRIOR ART

